

FIGURE 1

NON-EMPTY VIRTUAL OUTPUT QUEUES OF MULTISTAGE SWITCHES USING A PIPELINED ARBITRATION SCHEME"

Inventors: Eiji OKI et al.

Serial No.: 09/911,044; Docket No.: Poly-22-1/APP

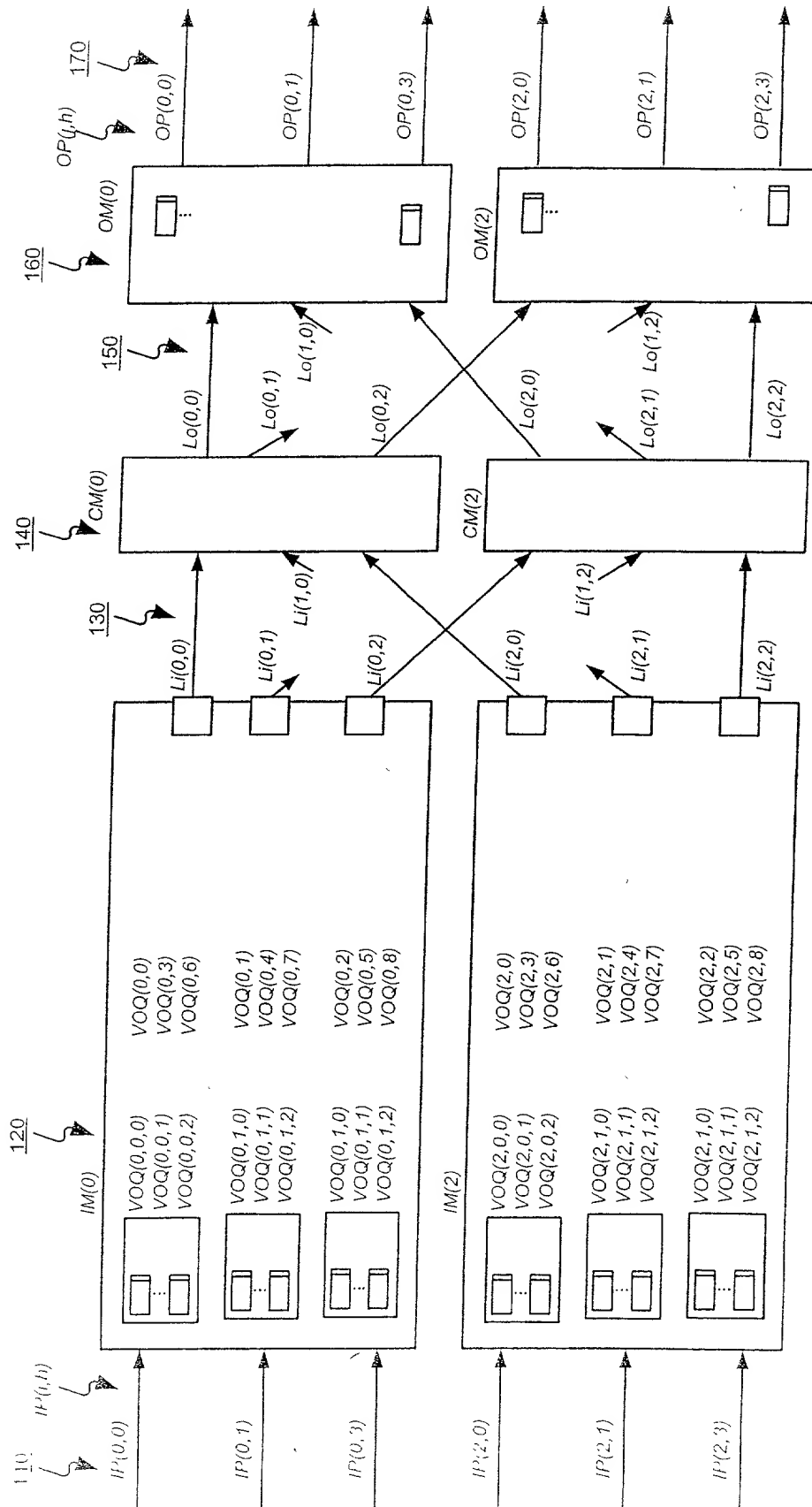
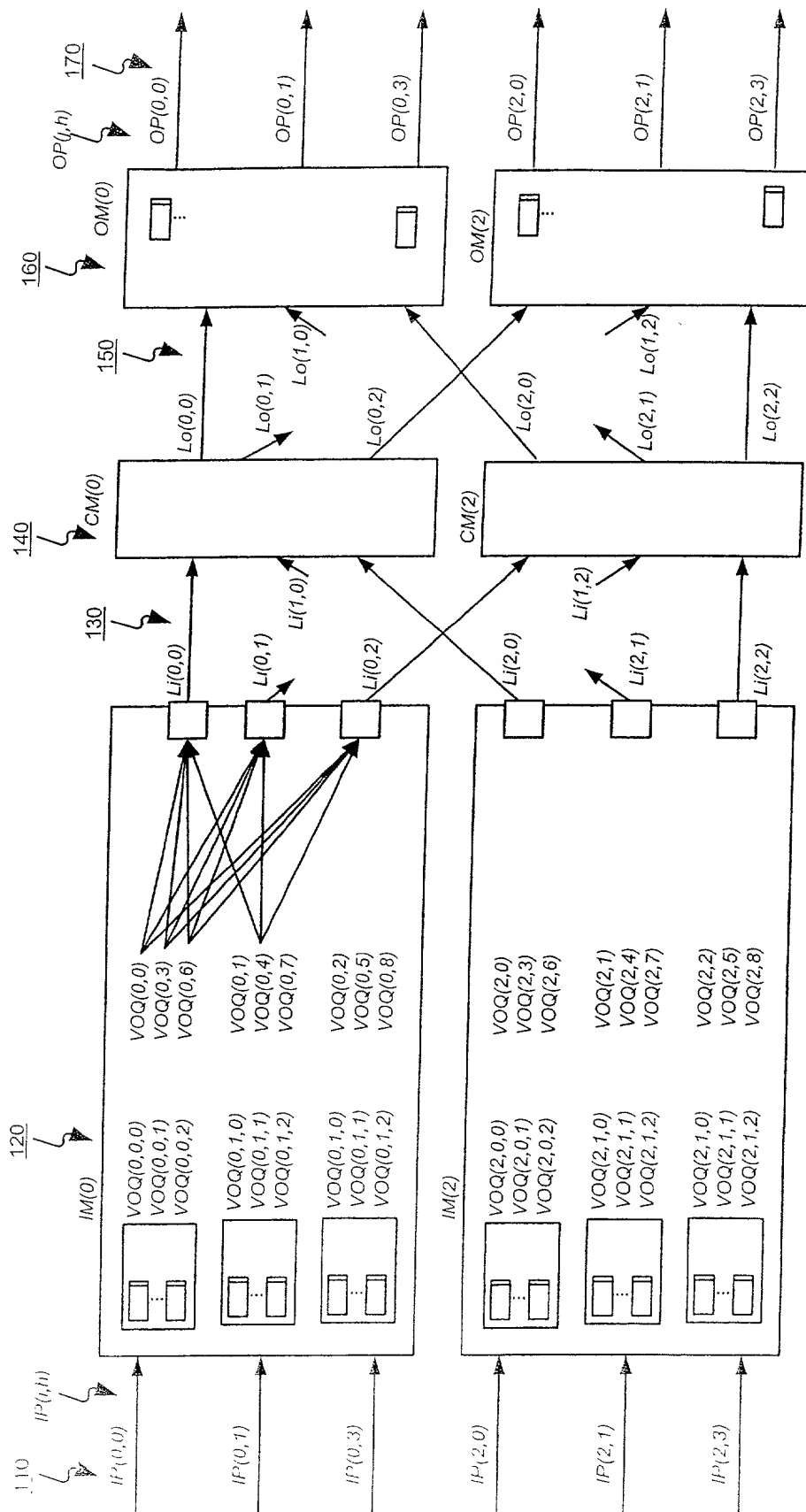


FIGURE 9a

FIG. 100' SCHEDULING THE DISPATCH OF CELLS IN
NON-EMPTY VIRTUAL OUTPUT QUEUES OF
MULTISTAGE SWITCHES USING A PIPELINED
ARBITRATION SCHEME"

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100'

FIGURE 9b

THE SCHEDULING THE DISPATCH OF CELLS IN
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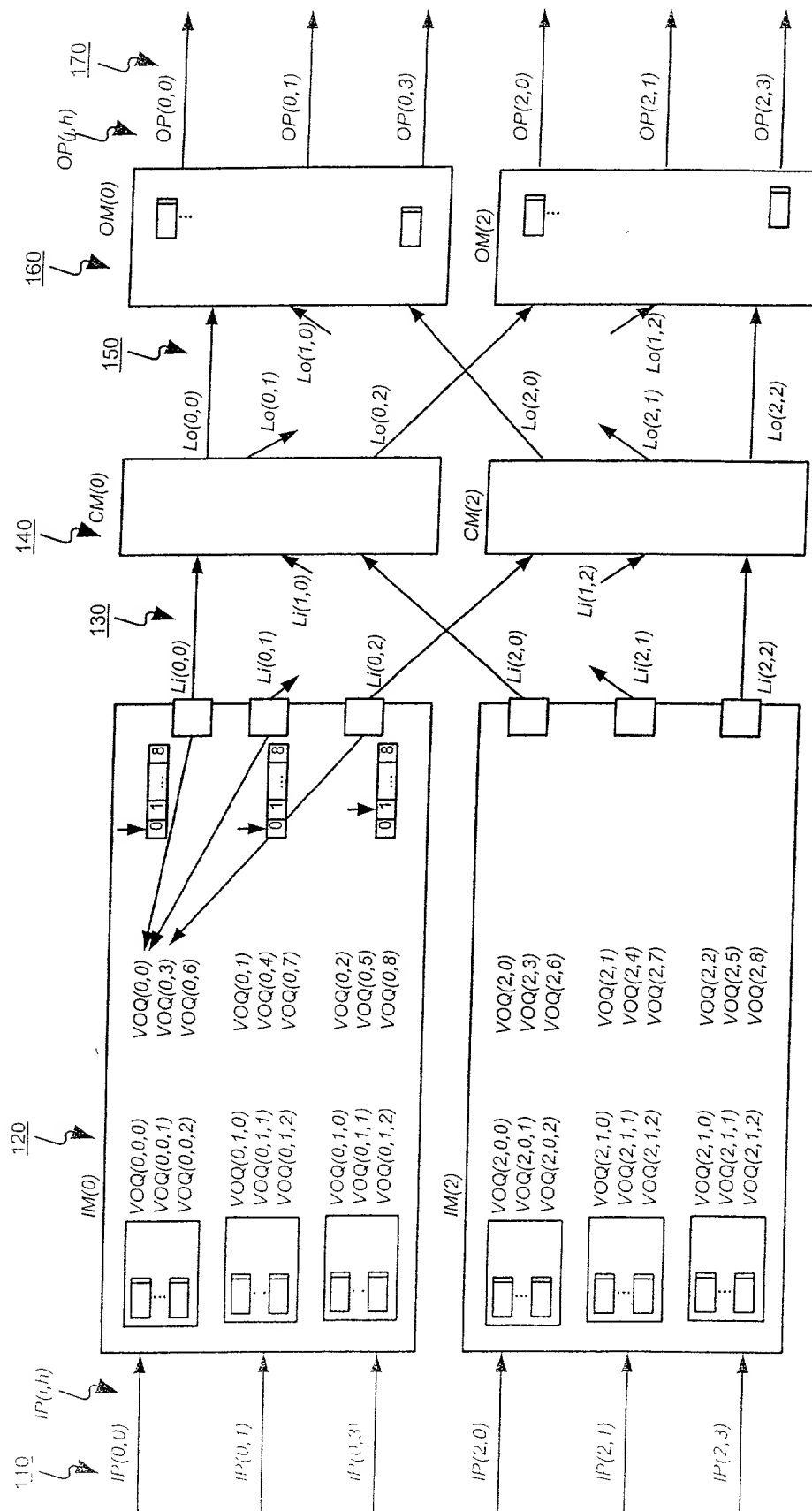
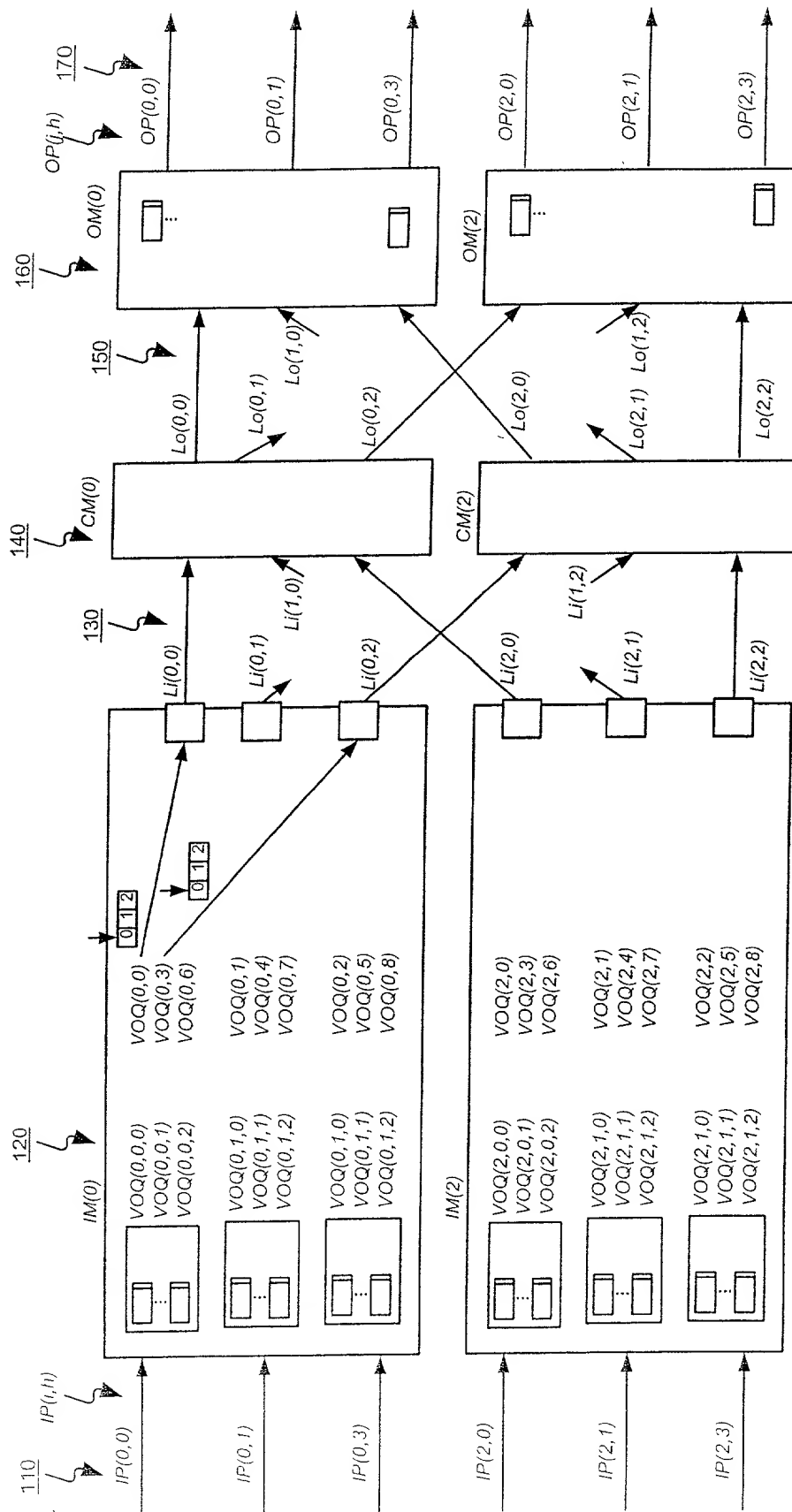


FIGURE 9c

Title: "SCHEDULING THE DISPATCH OF CELLS IN
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100'

FIGURE 9d

Title: "SCHEDULING THE DISPATCH OF CELLS IN
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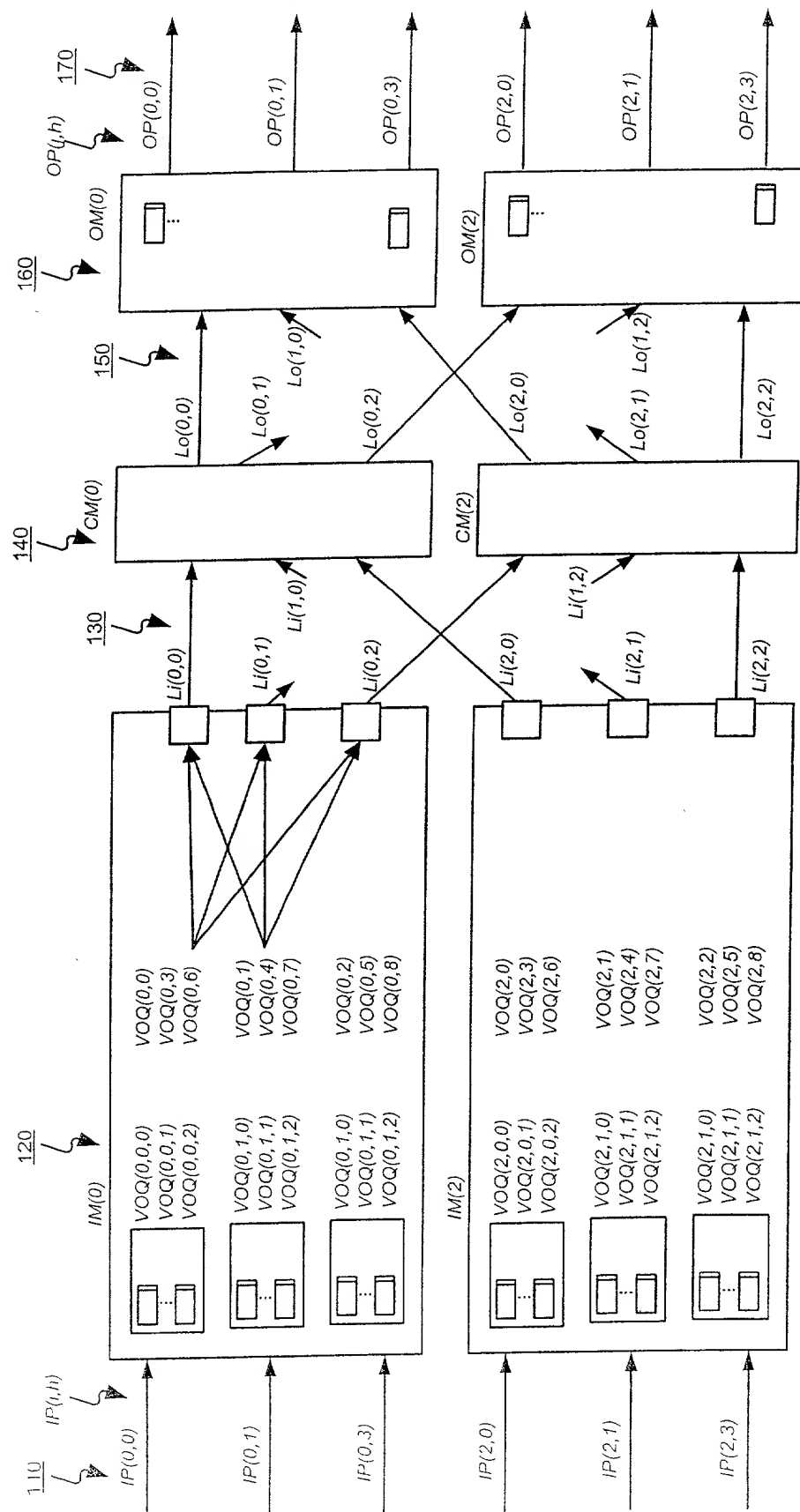


FIGURE 9e

Title: "SCHEDULING THE DISPATCH OF CELLS IN
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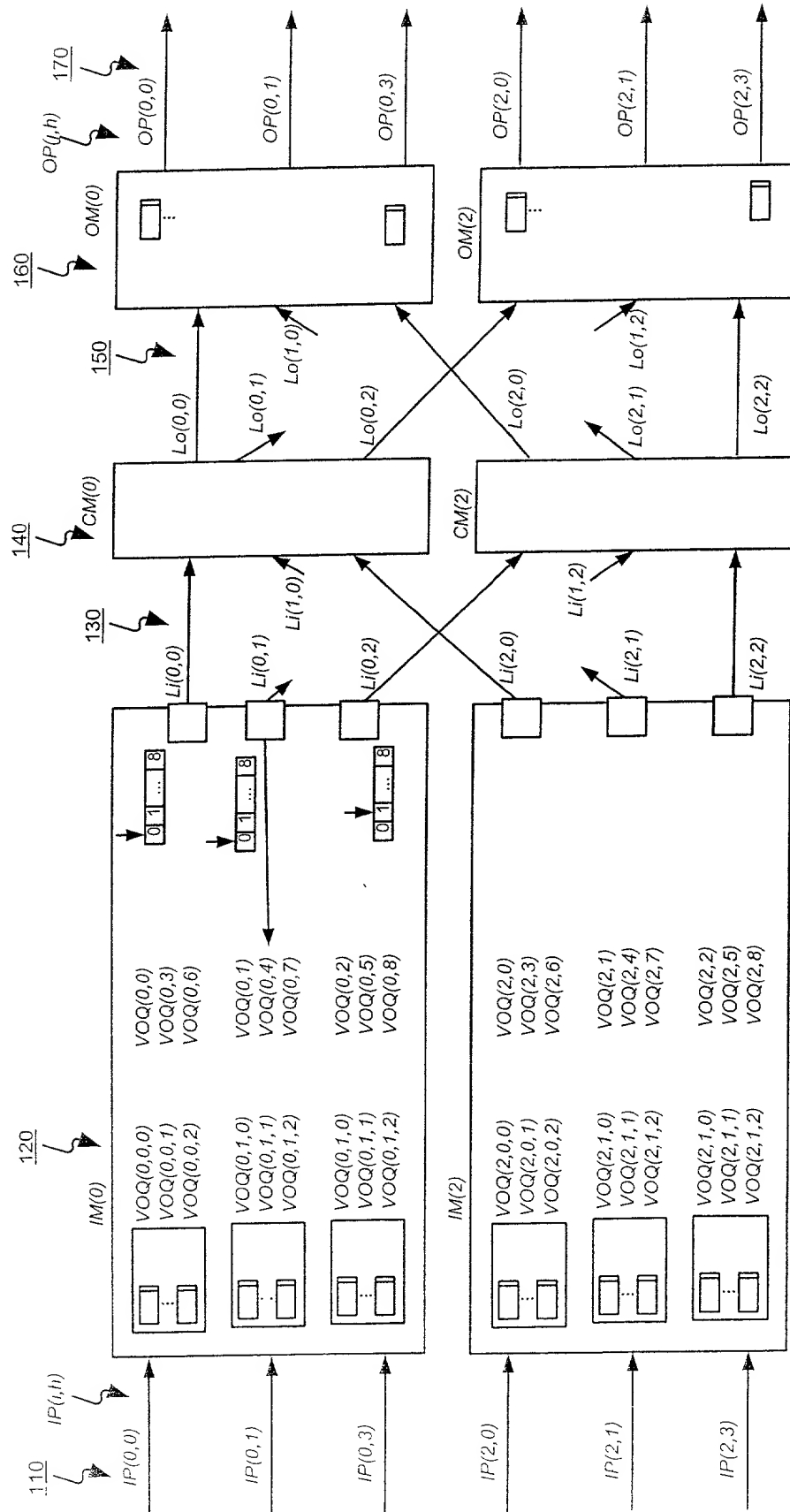


FIGURE 9f

Title: "SCHEDULING THE DISPATCH OF CELLS IN
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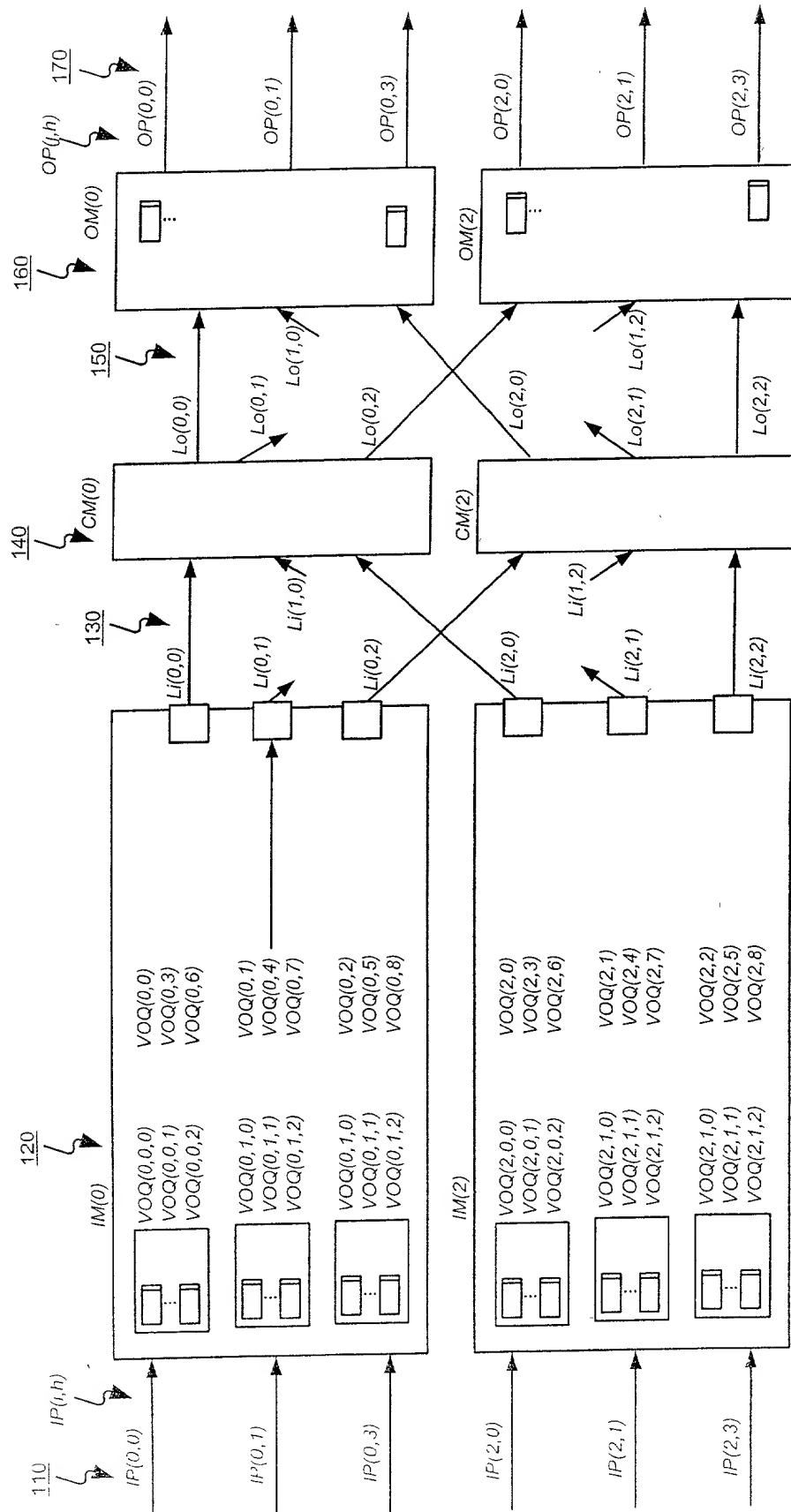
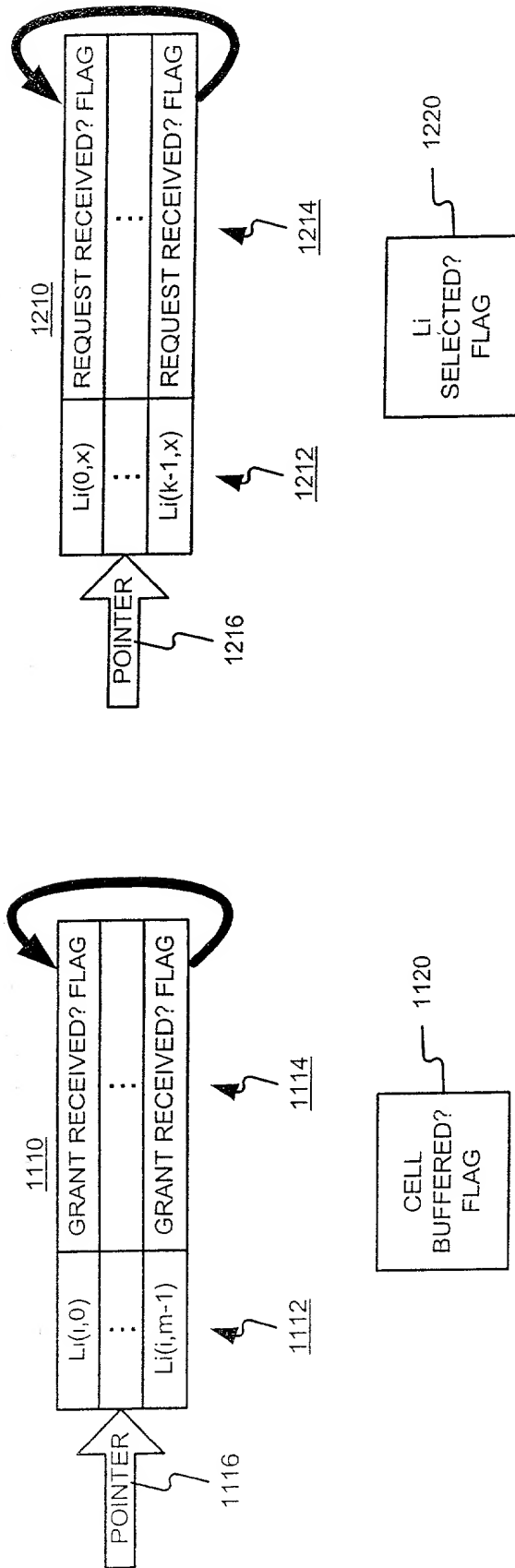
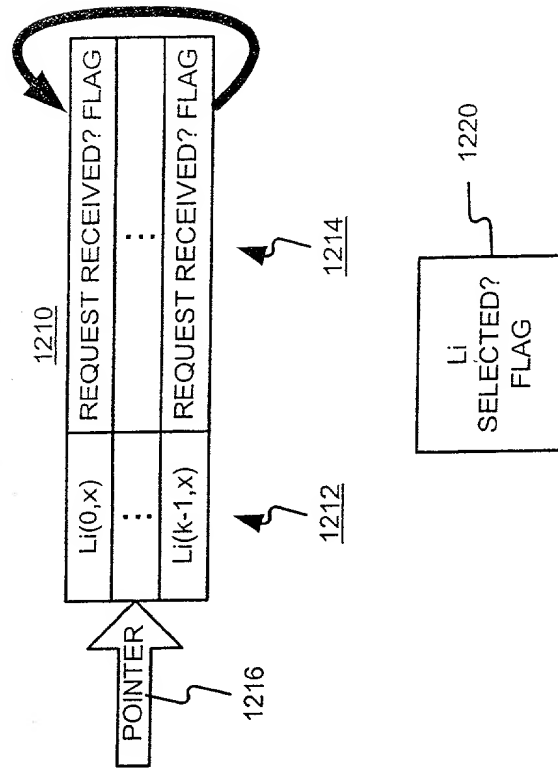


FIGURE 9g



245'
FIGURE 11



265'
FIGURE 12

FIG. 13. SCHEDULING THE DISPATCH OF CELLS IN
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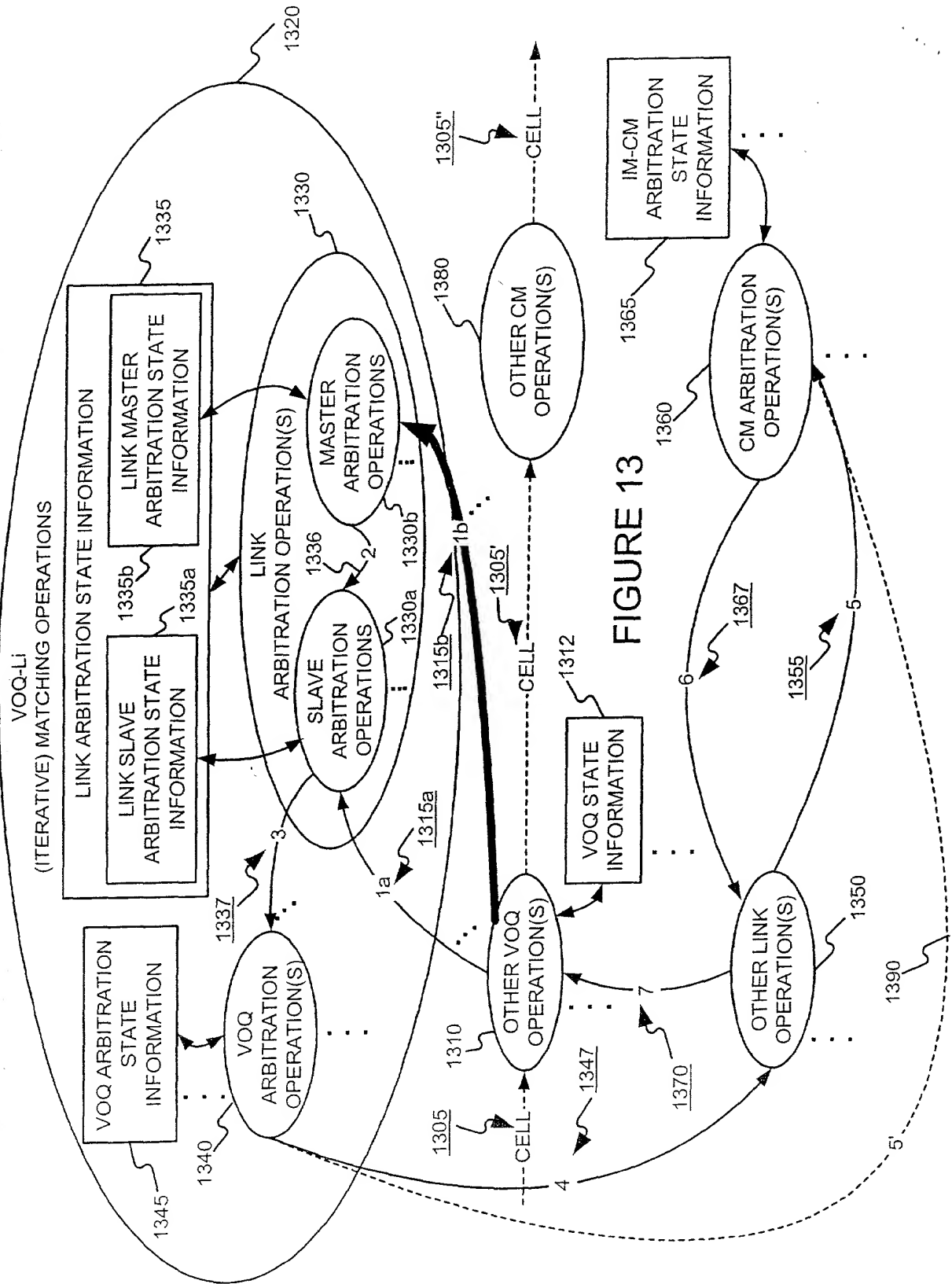


FIGURE 13

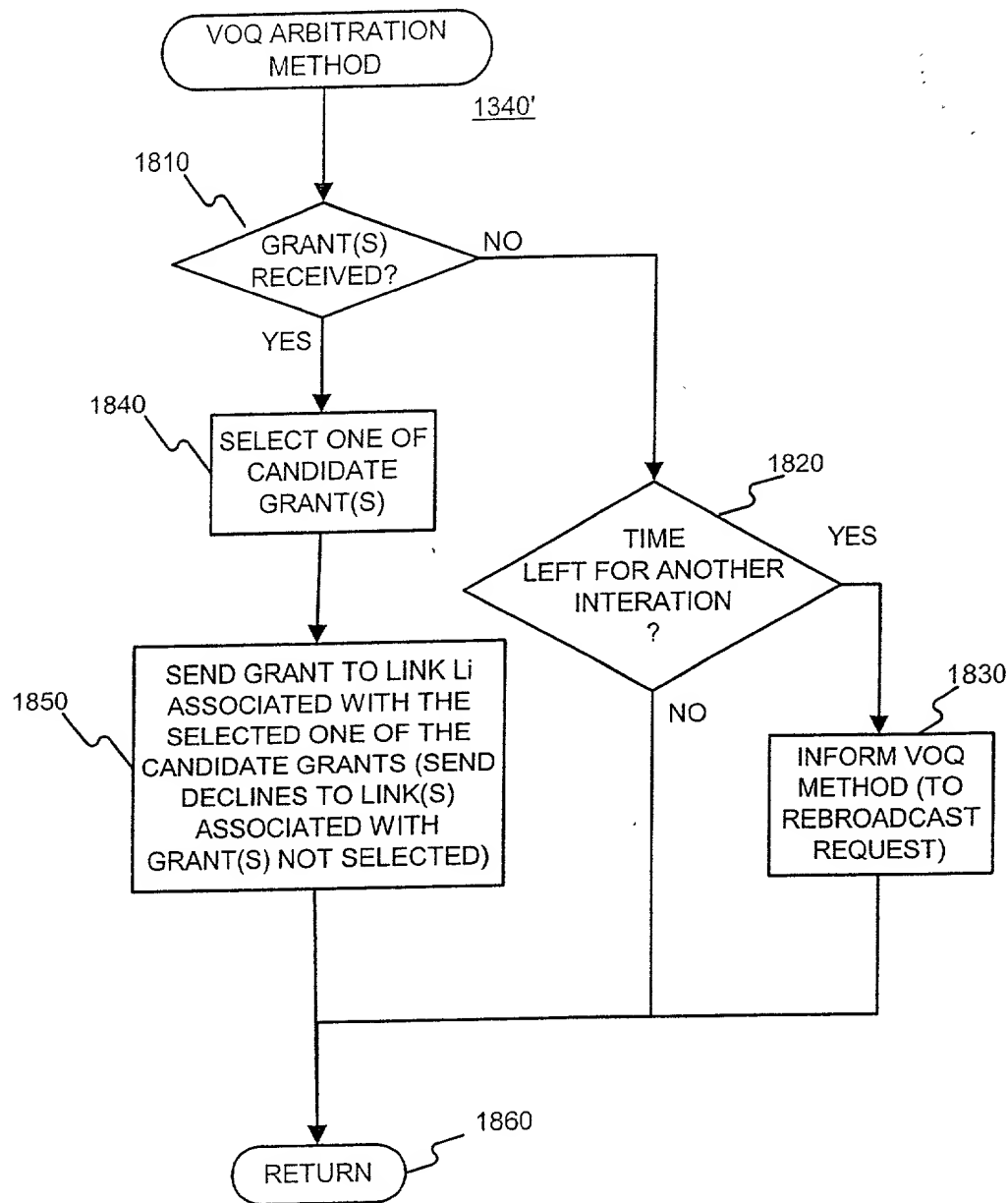


FIGURE 18

120'
 IM(0)

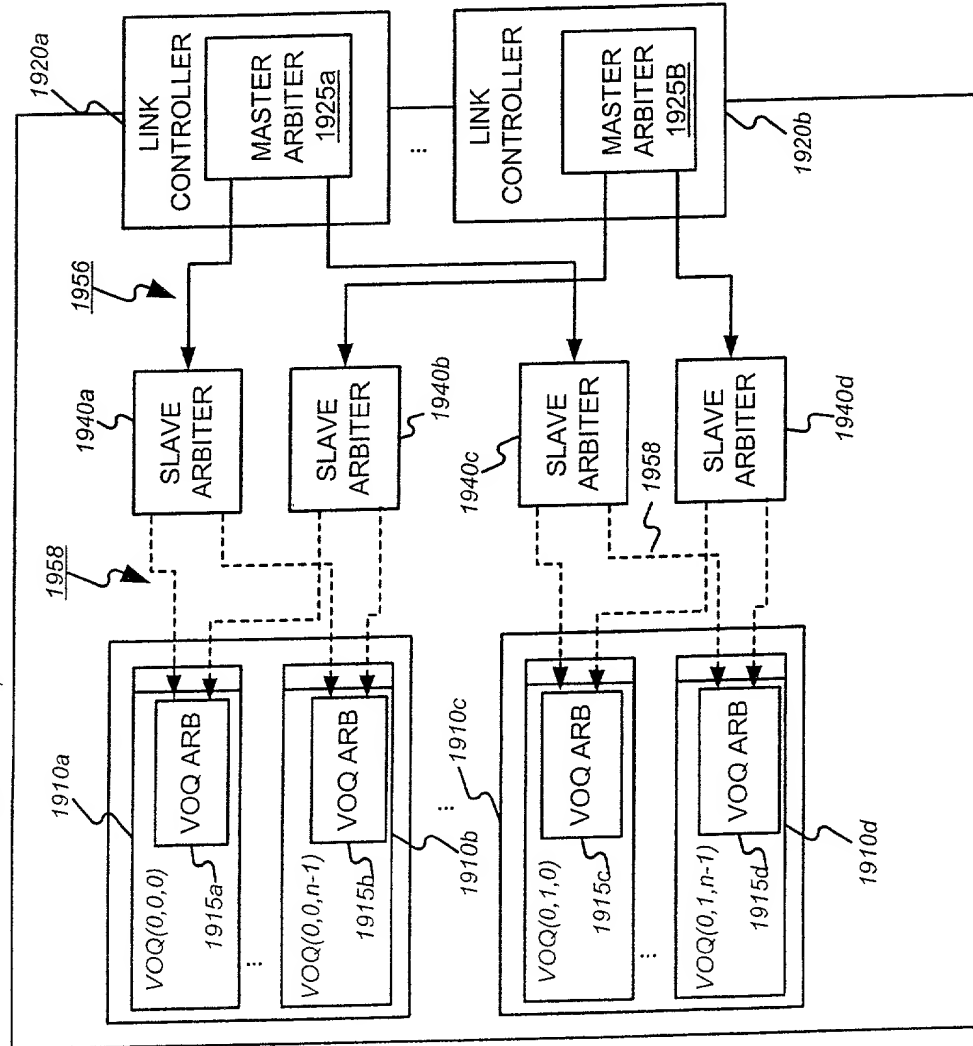
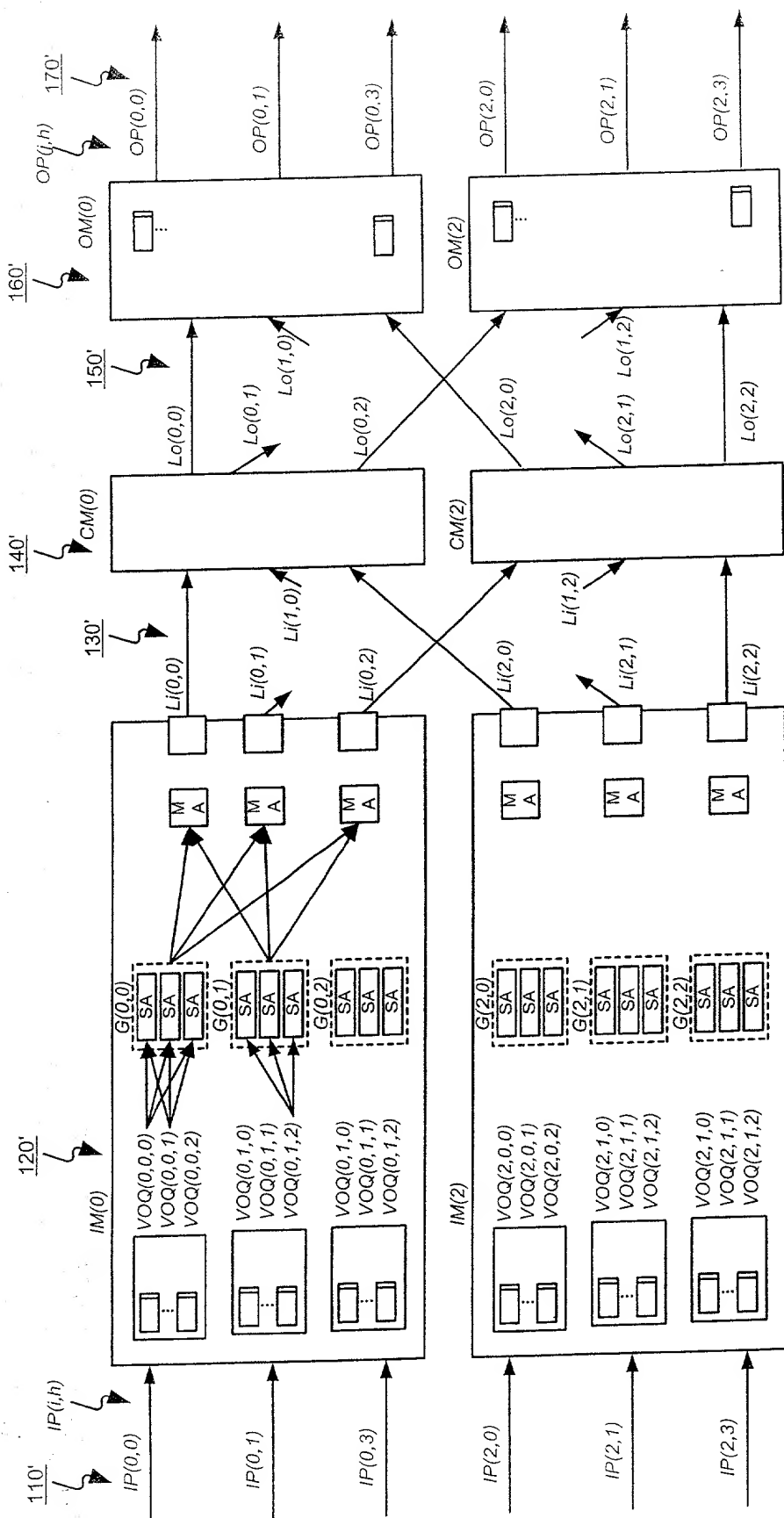


FIGURE 19b



100"
 FIGURE 20a

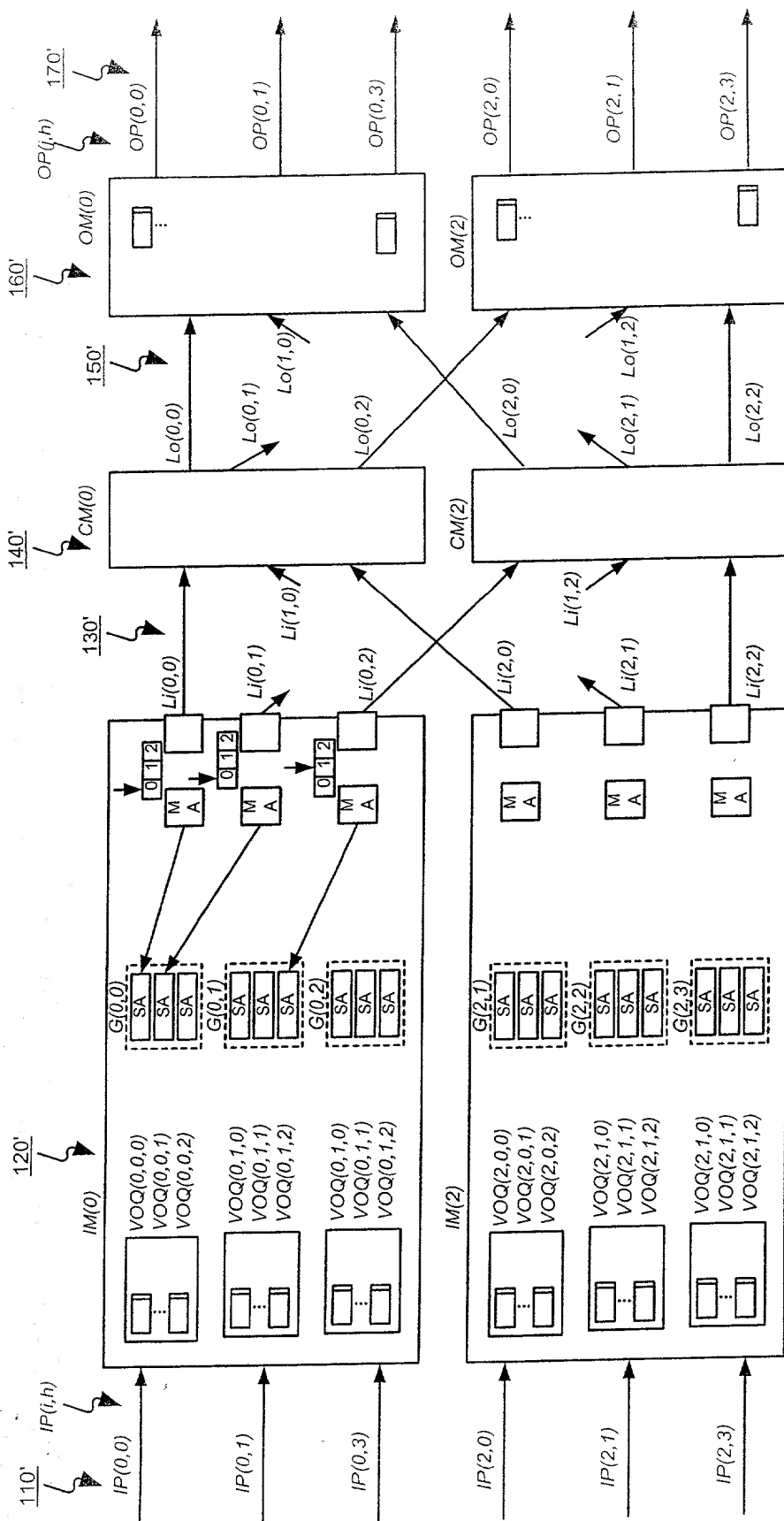


FIGURE 20b

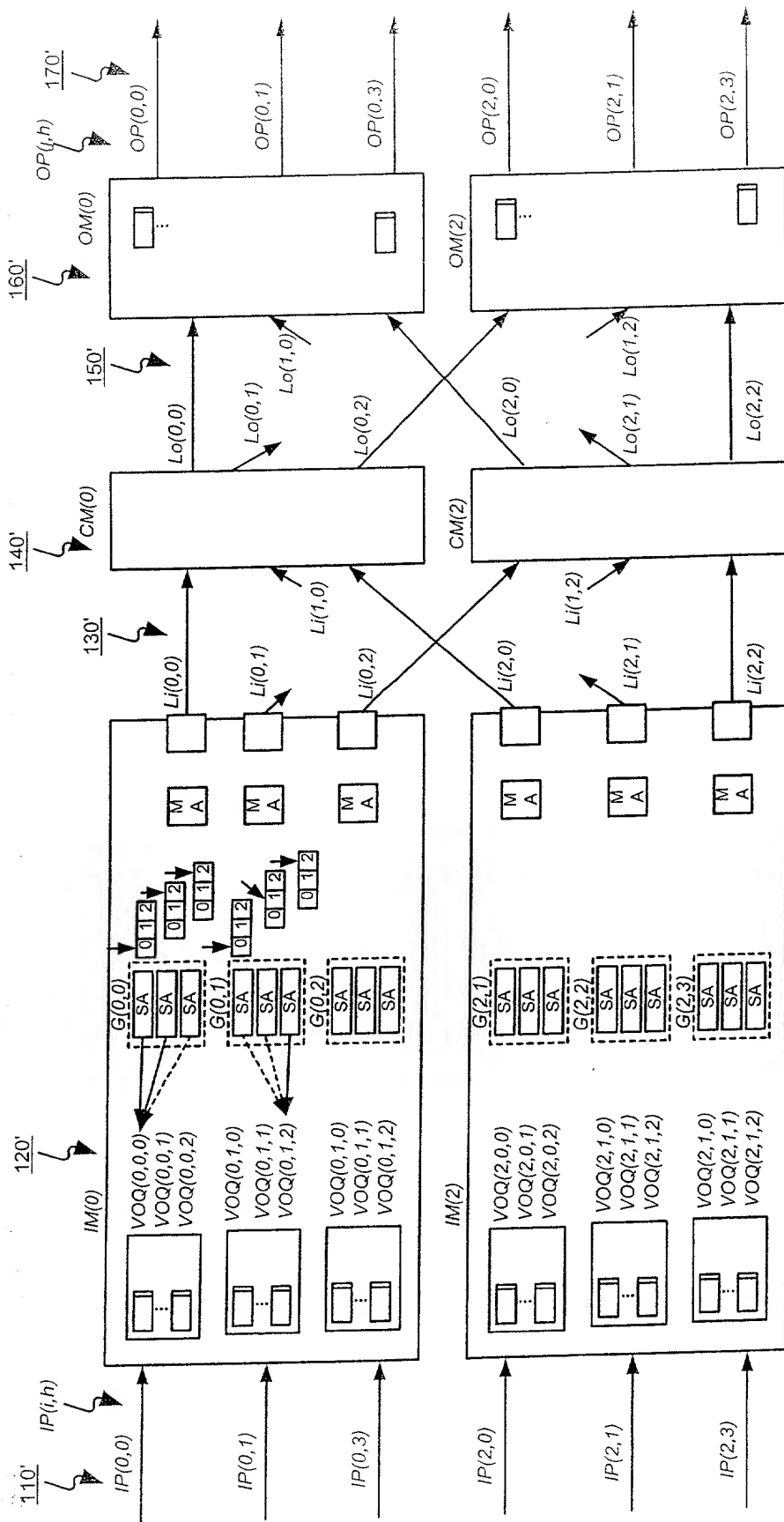
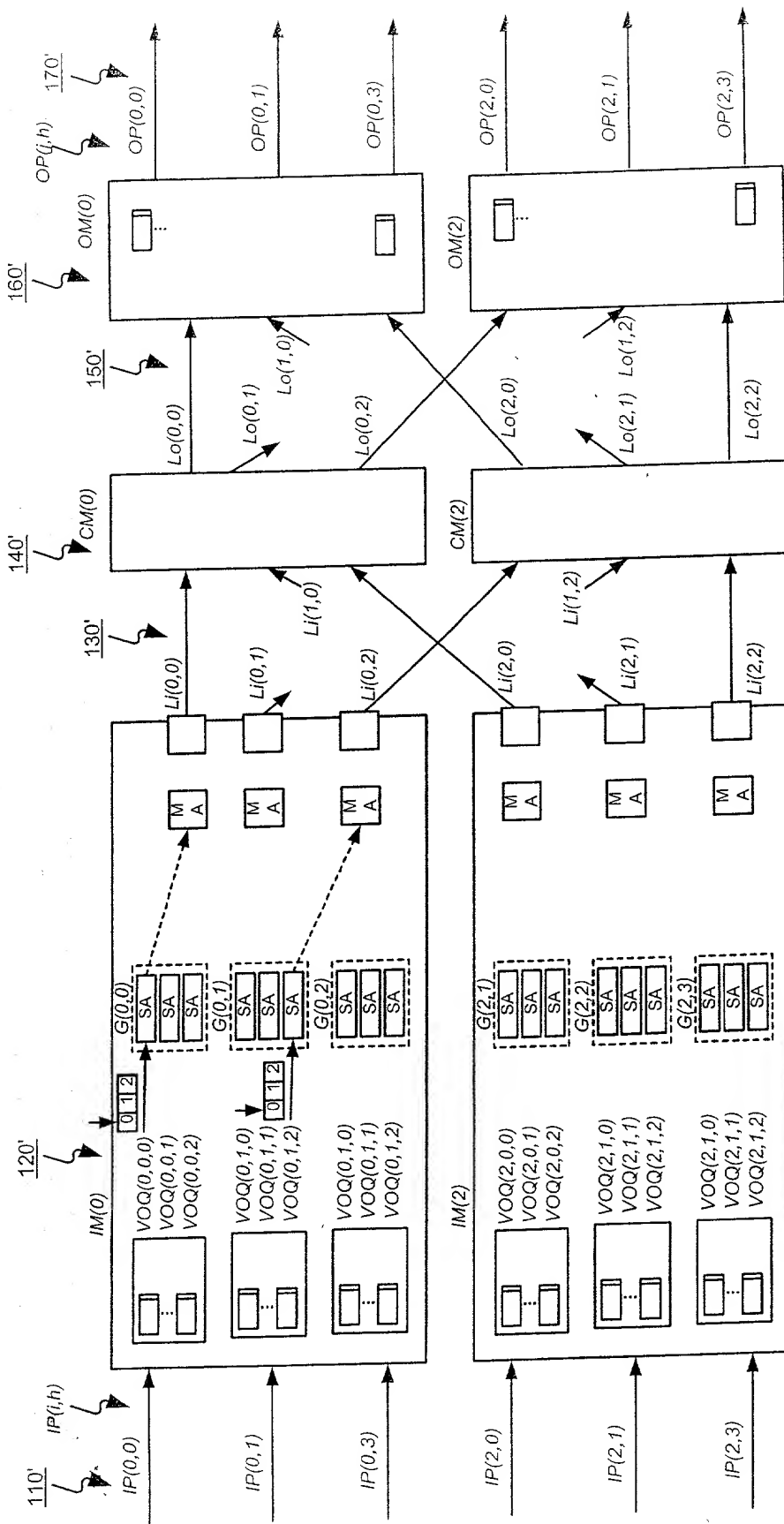
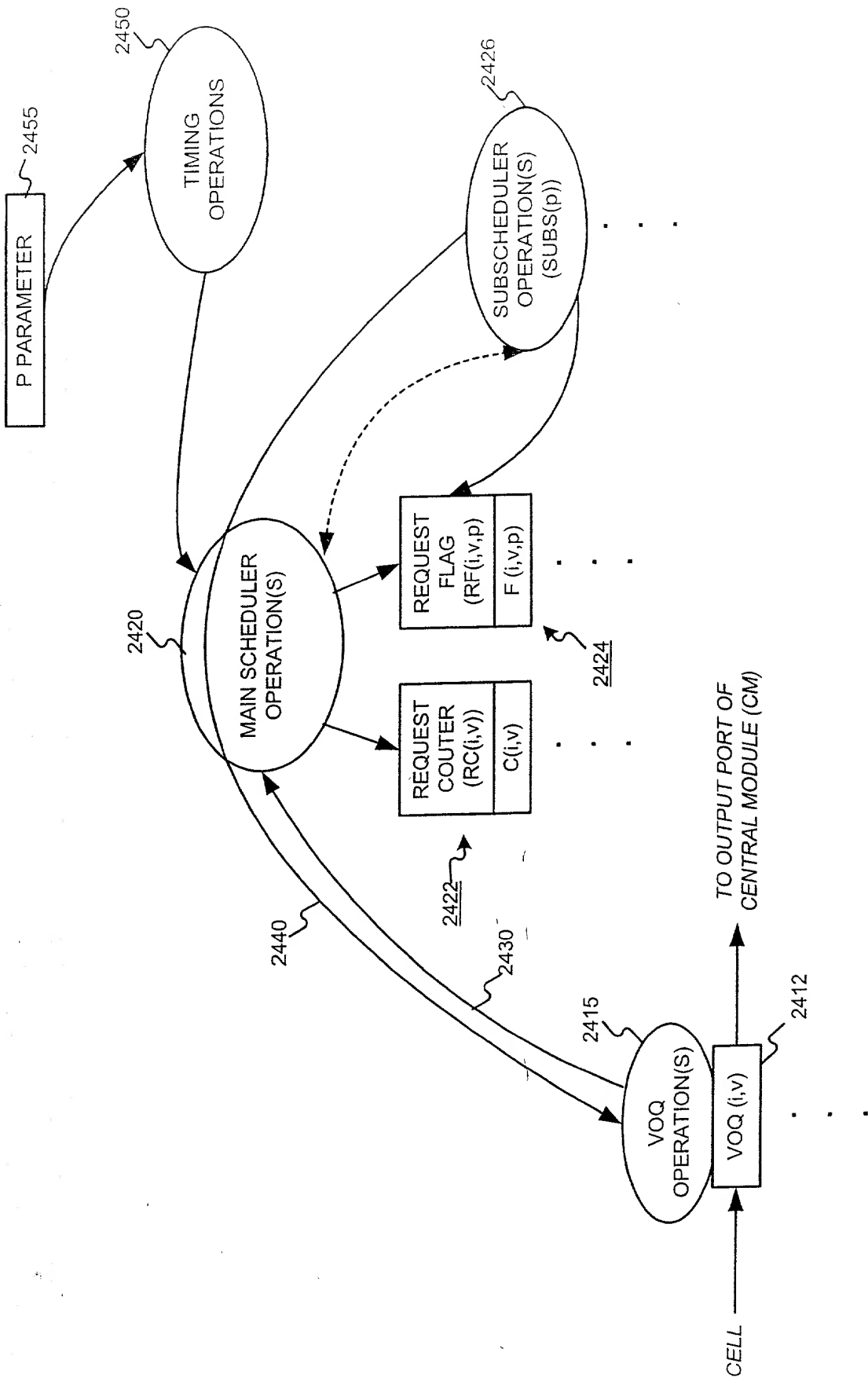


FIGURE 20c



100"
 FIGURE 20d



2400
FIGURE 24

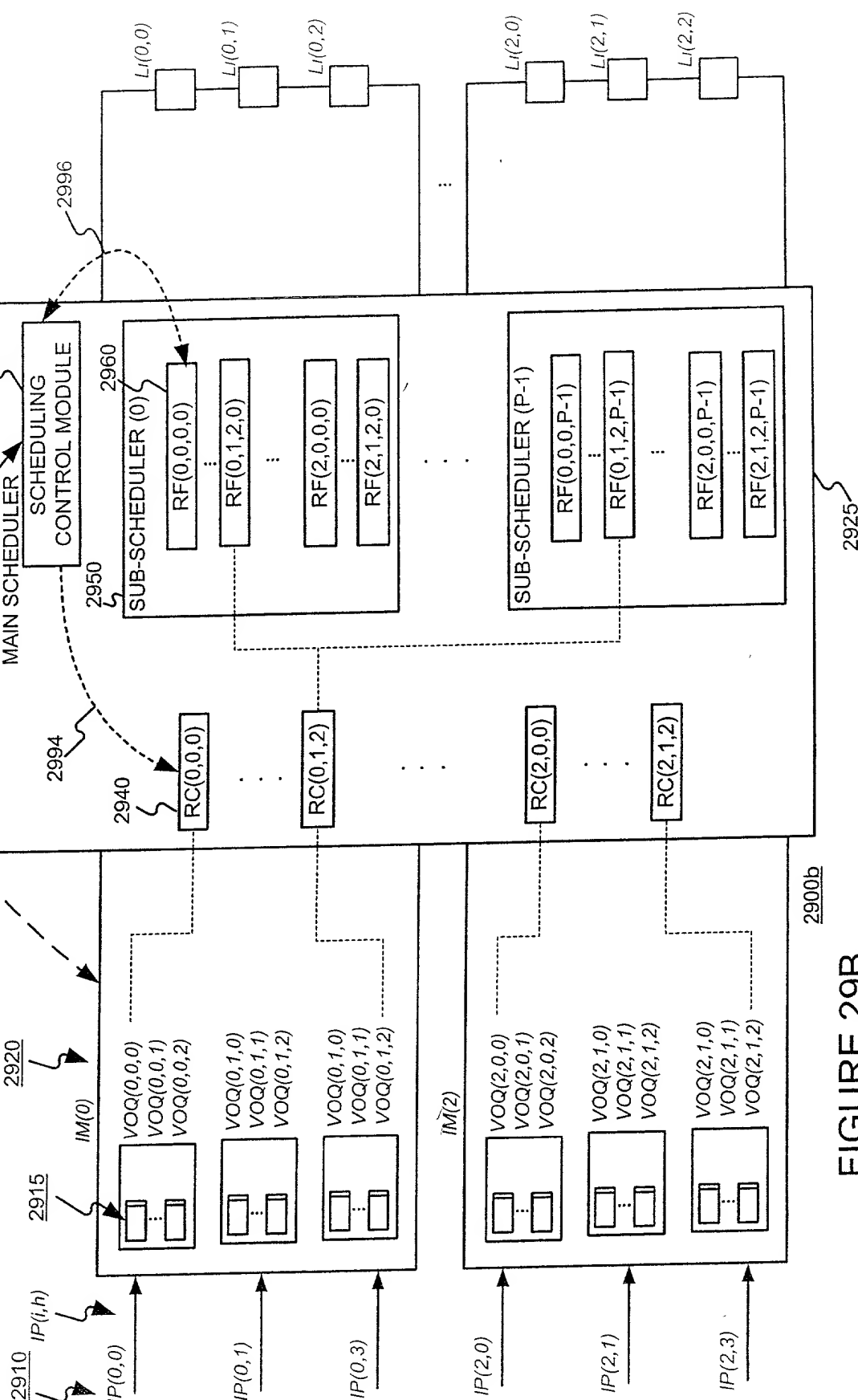


FIGURE 29B

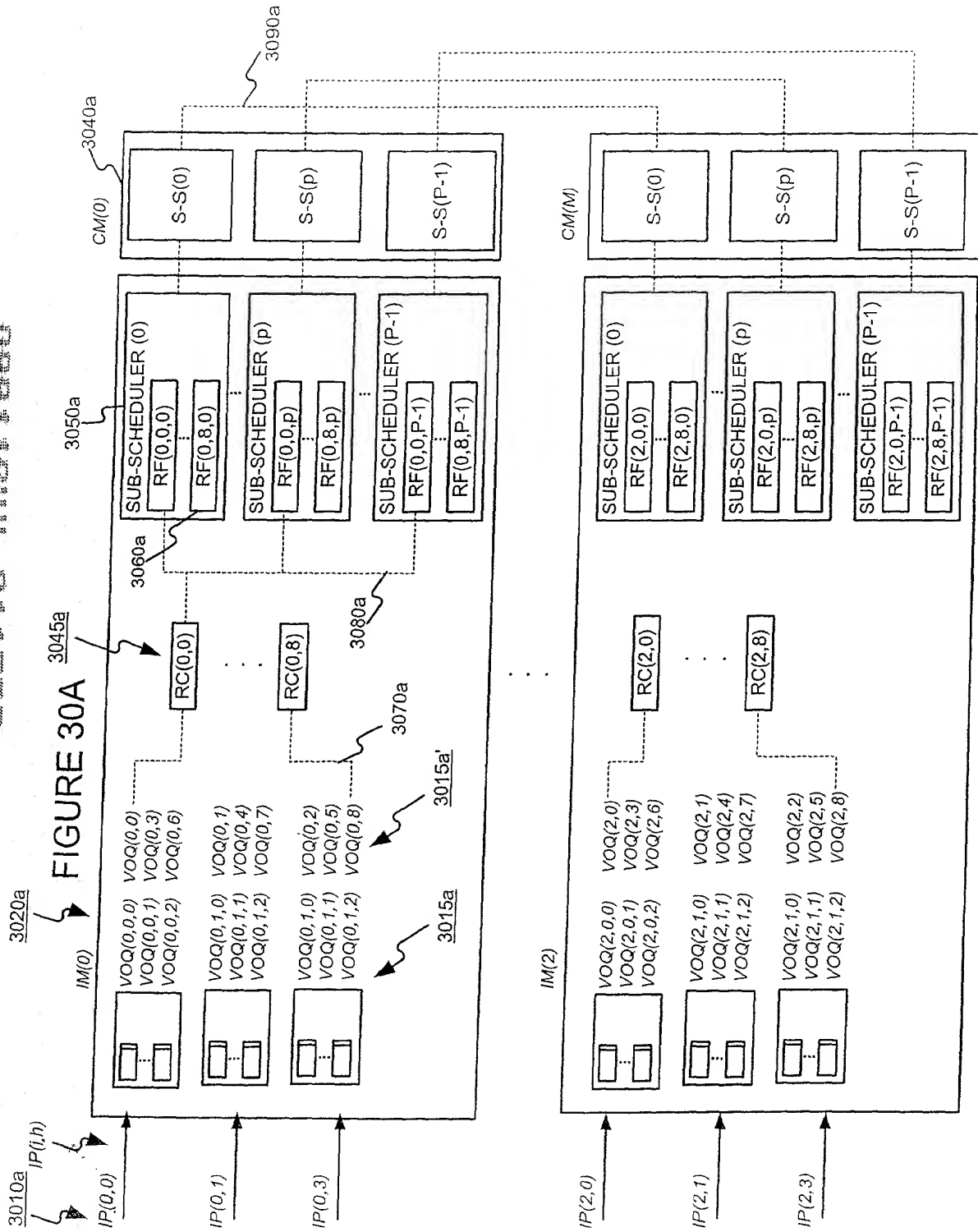
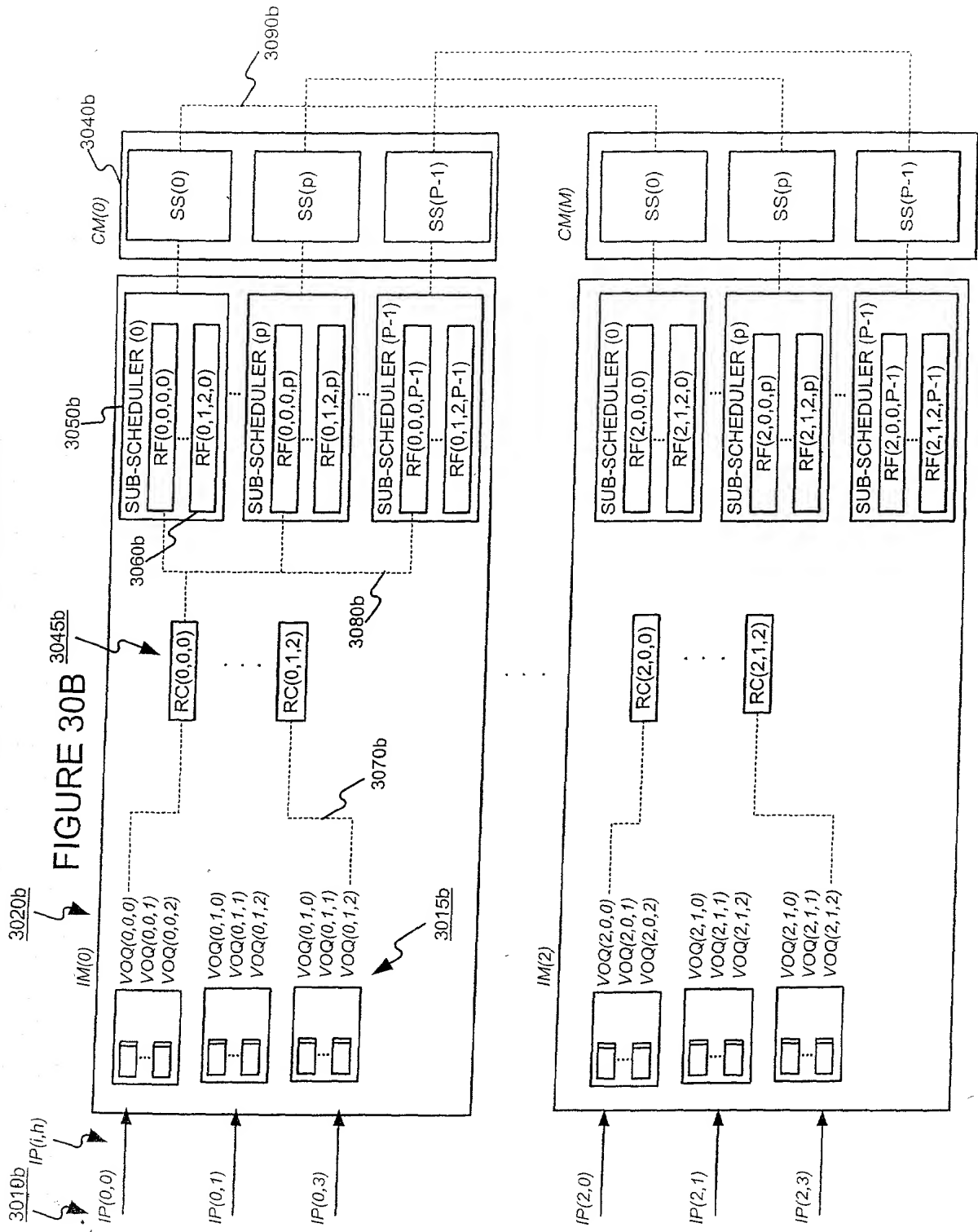


FIGURE 30B



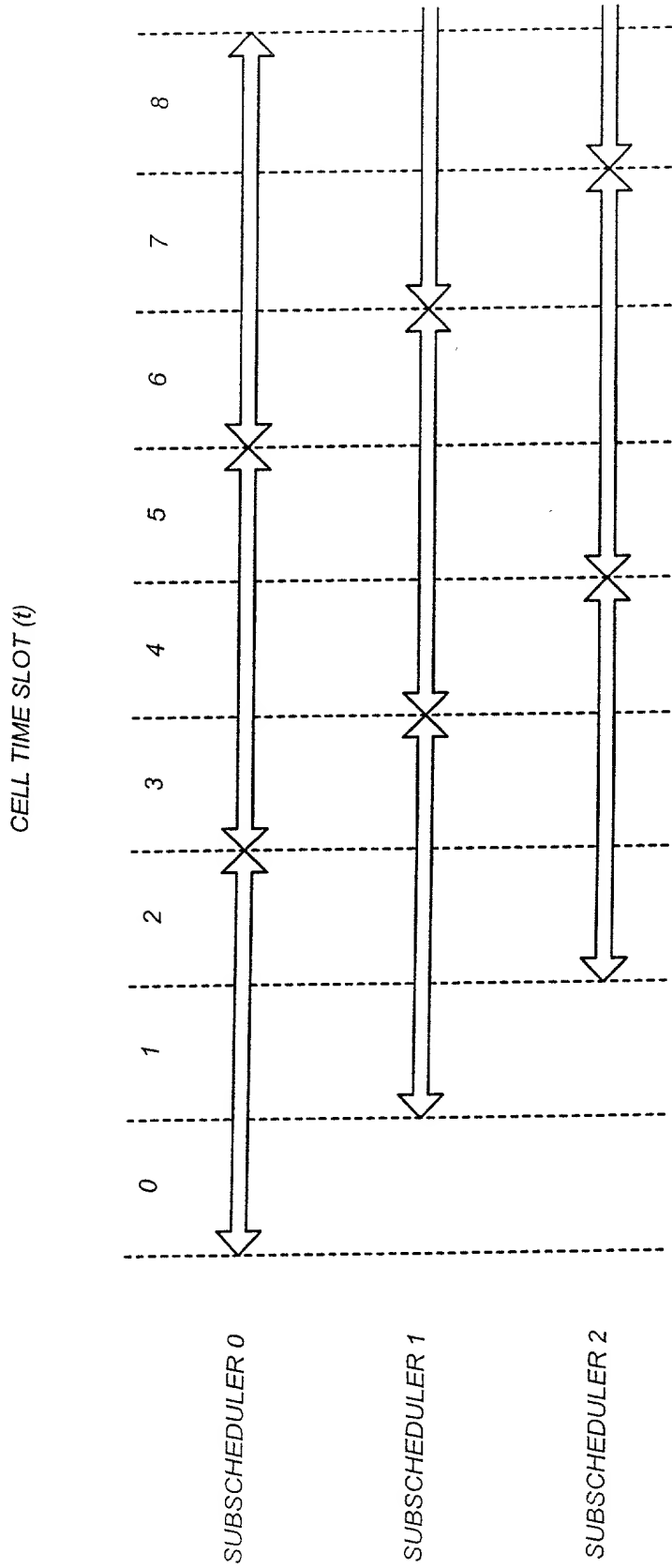


FIGURE 31